

REMARKS

On page 2 of the Action, it was held that newly submitted claims 13-15 are directed to an invention that is independent or distinct from the invention originally claimed.

The present invention contains several species, as shown in the drawings. However, claims 13-15 filed on June 15, 2006 are common to all the species, so that claims 13-15 are not separate and distinct species from the originally elected claims. Therefore, please examine claims 13-15 in the present application.

On pages 2-5 of the Action, claims 1-12 were rejected under 35 U.S.C. 102(b) as being anticipated by Hayashi.

In view of the rejection, claim 1 has been amended to clarify the structure of the invention.

In claim 1, it is defined that a micro power converter comprises, in part, a plurality of thin film magnetic induction components electrically connected to the semiconductor integrated circuit. Each of the thin film magnetic induction components comprises a magnetic insulation substrate and a coil conductor formed on the magnetic insulation substrate. One magnetic insulation substrate forming one thin film magnetic induction component with the coil conductor thereon is spaced with a gap from another magnetic insulation substrate forming another thin film magnetic induction component with the coil conductor thereon.

Namely, it is clearly defined that one magnetic insulation substrate with the coil conductor thereon is spaced with the gap from another magnetic insulation substrate with the coil conductor.

In the Action, it was held that "said plurality of thin film magnetic induction components being spaced apart from each other with a gap therebetween (0004 and layer 5)."

The paragraph 0004 of Hayashi is translated, as follows:

"[Subject to be solved by the Invention] The structure of a plan type magnetic induction element to be mounted on a

conventional small electric converter is formed, as shown in Fig. 4, such that a thin magnet layer 54 is formed on a thin coil 56, and spaces of the conduction members forming the thin coil 56 are filled with insulation filler (polyimide filler 55). In the drawing, 51 is silicon substrate, 52 is IC protection layer, 53 is lower insulation layer, 57 is upper insulation layer, 58 is magnetic thin layer, 61 is conventional plan type magnetic conductor element, and 62 is protection layer."

The layer 5 referred to by the Examiner is polyimide filler filled between the spaces of the conductor element spirally arranged. In Hayashi, the magnetic conductor element 61 or coil 6 is arranged spirally, and the filler 55 or 5 is filled in the space of the coil 6.

In claim 1, it is defiled that each of the thin film magnetic induction components comprises a magnetic insulation substrate and a coil conductor formed on the magnetic insulation substrate, and that one magnetic insulation substrate forming one thin film magnetic induction component with the coil conductor thereon is spaced with a gap from another magnetic insulation substrate forming another thin film magnetic induction component with the coil conductor thereon. In the invention, the gap is formed between the insulation substrates.

The structure in claim 1 is not disclosed in Hayashi.

In regard to claim 3, it was held in part, on page 3 of the Action, that "each of said thin film magnetic induction components includes one of said magnetic insulation substrates (8), a coil conductor formed on the one magnetic insulation substrate (6)."

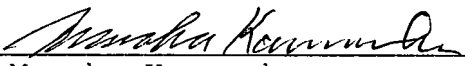
However, as explained in our response of June 15, 2006, numeral 8 referred to by the Examiner is a thick ferrite magnetic plate for preventing warping of the silicon substrate 1. The coil conductor is not formed on the magnetic insulation substrate, difference from claim 1.

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As explained above, claim 1 and its dependent claims are not anticipated by Hayashi and are patentable over Hayashi.

Reconsideration and allowance are earnestly solicited.

Respectfully Submitted,

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